

VARIABLES

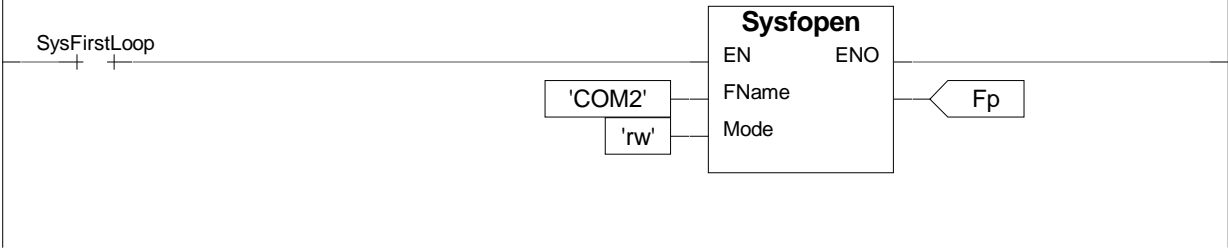
```
VAR_GLOBAL
TRPC26Read AT %MX100.16 : ARRAY[ 0..15 ] OF BOOL;
TRPC26Input : ARRAY[ 0..15 ] OF BOOL;
TRPC24Write AT %MX100.32 : ARRAY[ 0..15 ] OF BOOL;
TRPC24Output : ARRAY[ 0..15 ] OF BOOL;
END_VAR
```

	Project : IOOverTRP	
	VARIABLES :	
	Release :	Ver :1.00
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```
VAR
Start : BOOL; (* Comando di start modbus *)
Sequencer : USINT; (* Modbus sequencer *)
TRPC26Add : DB100AddOffset; (* DB 100 address calculation *)
Fp : FILEP; (* File pointer *)
SMode : SetSMode; (* Set serial mode *)
TRPC26Mdb : ModbusRTUMaster; (* TRPC26 modbus *)
TRPC24Mdb : ModbusRTUMaster; (* TRPC24 modbus *)
TRPC24Add : DB100AddOffset; (* DB 100 address calculation *)
END_VAR
```

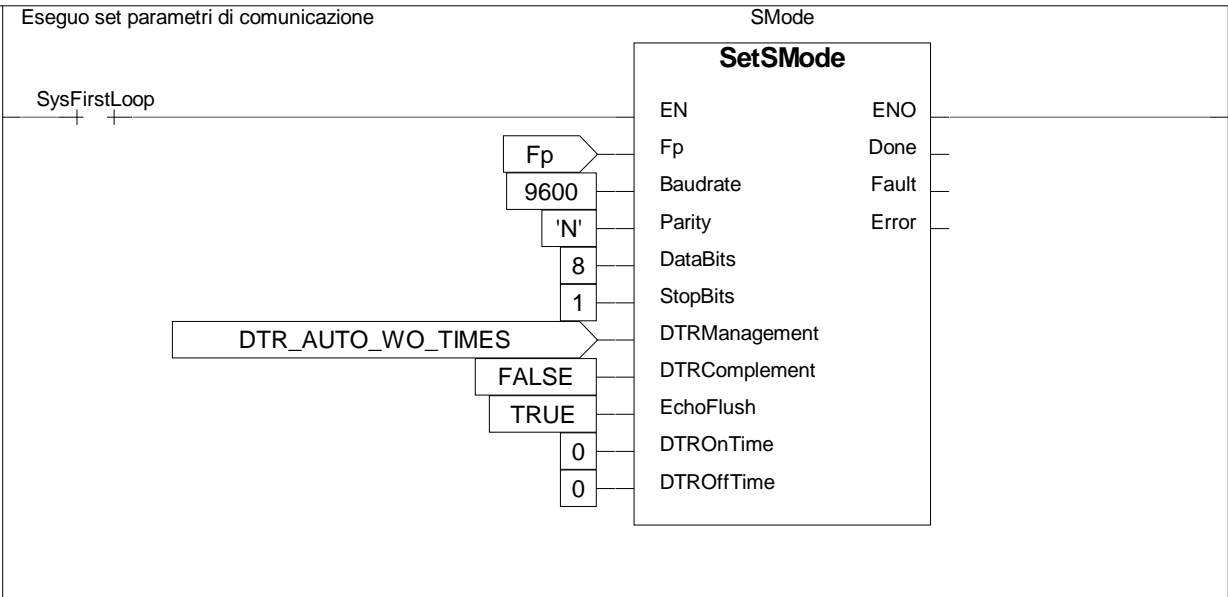
1

Eseguo set porta seriale da utilizzare



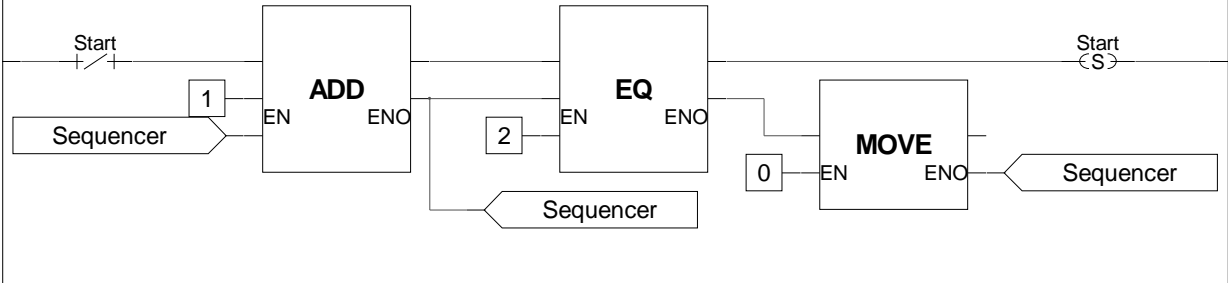
2

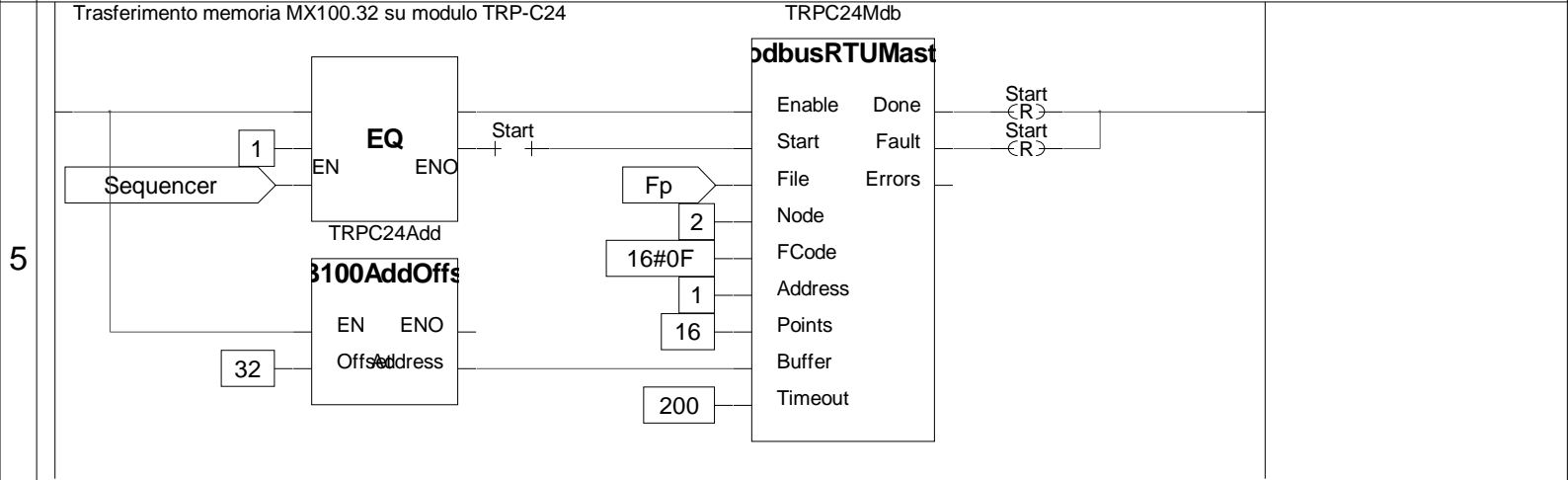
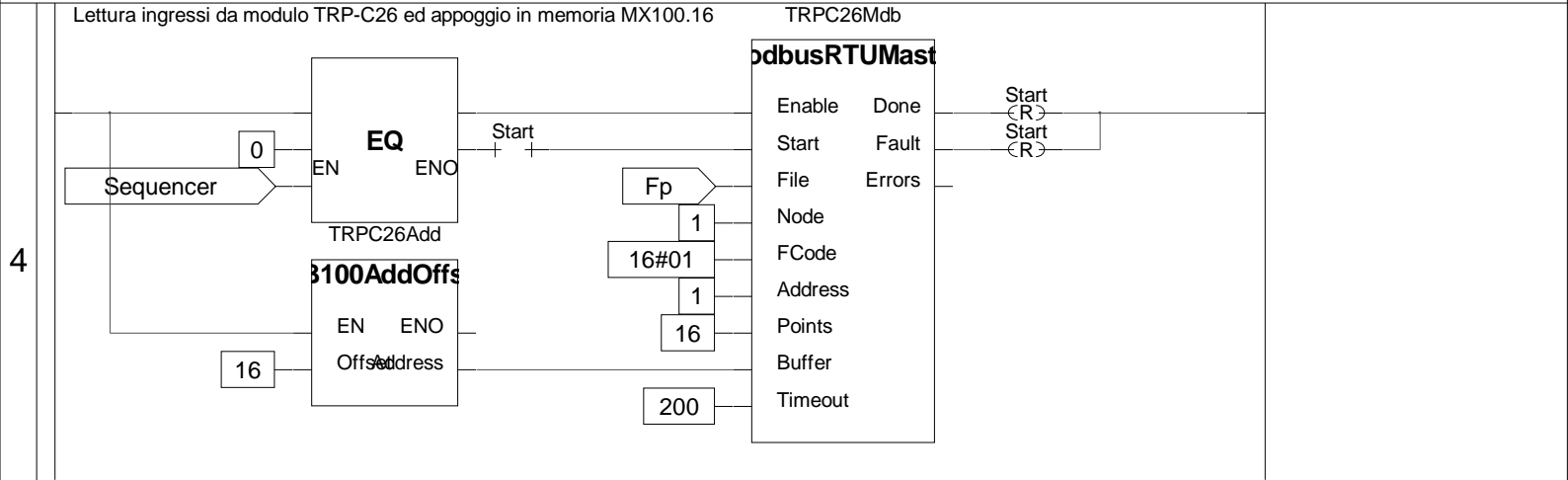
Eseguo set parametri di comunicazione



3

Sequenziatore letture registri





PROGRAM InputRead														
1	<div>TRPC26Read[8]</div> <div>TRPC26Input[0]</div>													
2	<div>TRPC26Read[9]</div> <div>TRPC26Input[1]</div>													
3	<div>TRPC26Read[10]</div> <div>TRPC26Input[2]</div>													
4	<div>TRPC26Read[11]</div> <div>TRPC26Input[3]</div>													
5	<div>TRPC26Read[12]</div> <div>TRPC26Input[4]</div>													
6	<div>TRPC26Read[13]</div> <div>TRPC26Input[5]</div>													
7	<div>TRPC26Read[14]</div> <div>TRPC26Input[6]</div>													
8	<div>TRPC26Read[15]</div> <div>TRPC26Input[7]</div>													
<table> <tr> <td rowspan="4"></td><td colspan="2">Project : IOOverTRP</td></tr> <tr> <td colspan="2">PROGRAM : InputRead</td></tr> <tr> <td>Release :</td><td>Ver :1.00</td></tr> <tr> <td>Author :</td><td>Date:30/11/2010</td></tr> <tr> <td></td><td>Note :</td><td>Page:1 of 2</td></tr> </table>				Project : IOOverTRP		PROGRAM : InputRead		Release :	Ver :1.00	Author :	Date:30/11/2010		Note :	Page:1 of 2
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	PROGRAM : InputRead													
	Release :	Ver :1.00												
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9	TRPC26Read[0] + / +	TRPC26Input[8] €)
10	TRPC26Read[1] + / +	TRPC26Input[9] €)
11	TRPC26Read[2] + / +	TRPC26Input[10] €)
12	TRPC26Read[3] + / +	TRPC26Input[11] €)
13	TRPC26Read[4] + / +	TRPC26Input[12] €)
14	TRPC26Read[5] + / +	TRPC26Input[13] €)
15	TRPC26Read[6] + / +	TRPC26Input[14] €)
16	TRPC26Read[7] + / +	TRPC26Input[15] €)

Project : IOOverTRP

PROGRAM : InputRead

Release :

Ver :1.00

Author :

Date:30/11/2010

Note :

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PROGRAM OutputWrite		
1	<div> <div>TRPC24Output[8]</div> <div>TRPC24Write[0]</div> </div>	
2	<div> <div>TRPC24Output[9]</div> <div>TRPC24Write[1]</div> </div>	
3	<div> <div>TRPC24Output[10]</div> <div>TRPC24Write[2]</div> </div>	
4	<div> <div>TRPC24Output[11]</div> <div>TRPC24Write[3]</div> </div>	
5	<div> <div>TRPC24Output[12]</div> <div>TRPC24Write[4]</div> </div>	
6	<div> <div>TRPC24Output[13]</div> <div>TRPC24Write[5]</div> </div>	
7	<div> <div>TRPC24Output[14]</div> <div>TRPC24Write[6]</div> </div>	
8	<div> <div>TRPC24Output[15]</div> <div>TRPC24Write[7]</div> </div>	
<div> <div></div> <div> <div>Project : IOOverTRP</div> <div>PROGRAM : OutputWrite</div> <div>Release :Ver :1.00</div> <div>Author :Date:30/11/2010</div> <div>Note :Page:1 of 2</div> </div> </div>		

9	TRPC24Output[0] + +	TRPC24Write[8] ⌢ ⌣
10	TRPC24Output[1] + +	TRPC24Write[9] ⌢ ⌣
11	TRPC24Output[2] + +	TRPC24Write[10] ⌢ ⌣
12	TRPC24Output[3] + +	TRPC24Write[11] ⌢ ⌣
13	TRPC24Output[4] + +	TRPC24Write[12] ⌢ ⌣
14	TRPC24Output[5] + +	TRPC24Write[13] ⌢ ⌣
15	TRPC24Output[6] + +	TRPC24Write[14] ⌢ ⌣
16	TRPC24Output[7] + +	TRPC24Write[15] ⌢ ⌣

		Project : IOOverTRP	
		PROGRAM : OutputWrite	
		Release :	Ver :1.00
		Author :	Date:30/11/2010
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PROGRAM Logic

1	TRPC26Input[0]	TRPC24Output[0]
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[illegible]

	Project : IOOverTRP
	PROGRAM : Logic

Release :	Ver :1.00
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Author :	Date:30/11/2010
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