

VARIABLES

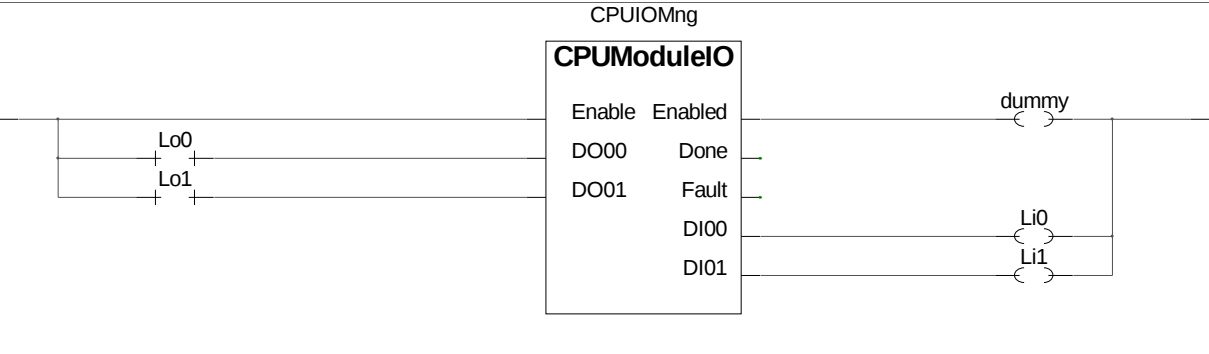
VAR_GLOBAL

```
Li0 AT %MX100.16 : BOOL; (* Ingresso 0 modulo CPU *)
Li1 AT %MX100.17 : BOOL; (* Ingresso 1 modulo CPU *)
Lo0 AT %MX100.32 : BOOL; (* Uscita 0 modulo CPU *)
Lo1 AT %MX100.33 : BOOL; (* Uscita 1 modulo CPU *)
END_VAR
```

	Project : PLCMaster	
	VARIABLES :	
	Release : PLCMaster	Ver :1.00
	Author :	Date:10/04/2012
	Note :	Page:1 of 1

```
VAR
CPUiOMng : CPUModuleIO;
dummy : BOOL;
END_VAR
```

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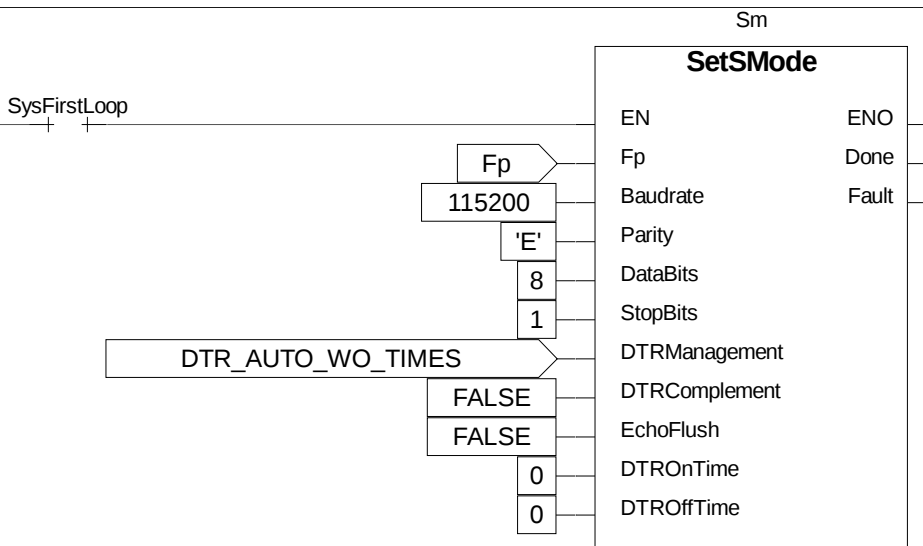
	Project : PLCMaster	
	PROGRAM : CPUIO	
	Release : PLCMaster	Ver :1.00
	Author :	Date:10/04/2012
	Note :	Page:1 of 1

```
VAR
Fp : FILEP; (* File pointer alla seriale *)
Sm : SetSMode; (* FB set serial mode *)
Done : ARRAY[ 0..1 ] OF BOOL; (* Modbus master done *)
MdbRd : sModbusRTUMaster; (* FB modbus master (Lettura ingressi) *)
MdbWr : sModbusRTUMaster; (* FB modbus master (Scrittura uscite) *)
END_VAR
```

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Project : PLCMaster

PROGRAM : MdbMaster

Release : PLCMaster

Ver :1.00

Author :

Date:10/04/2012

Note :

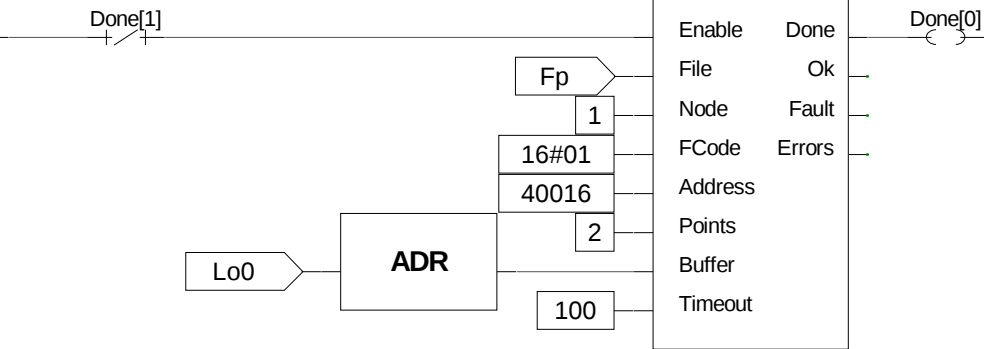
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Lettura ingressi da sistema slave, trasferisco su uscite logiche master

MdbRd

Attenzione! dovendo leggere dallo slave MX100.16 setto adress 40016

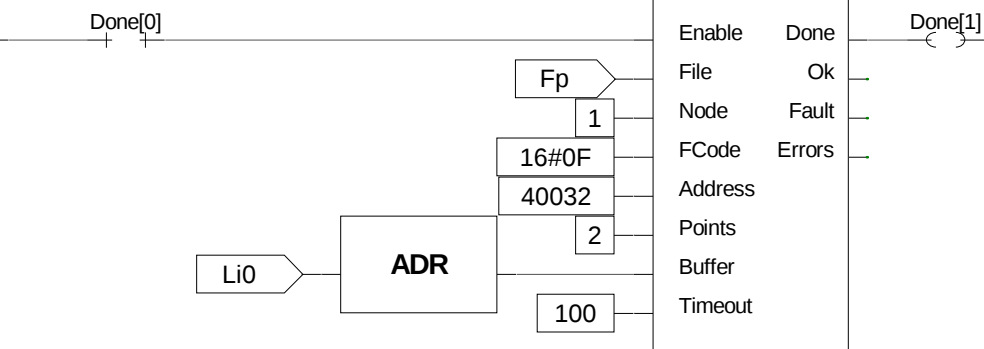


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Lettura ingressi da sistema master, trasferisco su uscite del sistema slave

MdbWr

Attenzione! dovendo scrivere sullo slave MX100.32 setto adress 40032



Project : PLCMaster

PROGRAM : MdbMaster

Release : PLCMaster

Ver :1.00

Author :

Date:10/04/2012

Note :

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(SFR054B000) Manages the modbus RTU master communication
ENCRYPTED CODE

```
VAR_INPUT
Enable : BOOL;
File : FILEP; (* Terminal I/O pointer *)
Node : USINT; (* Node number *)
FCode : USINT; (* Function code *)
Address : UINT; (* Start address *)
Points : USINT; (* Number of points *)
Buffer : @USINT; (* Address of data buffer *)
Timeout : UINT; (* Timeout time (mS) *)
END_VAR

VAR_OUTPUT
Done : BOOL; (* Command done *)
Ok : BOOL := FALSE; (* Execution Ok *)
Fault : BOOL; (* Command fault *)
Errors : UDINT; (* Error counter *)
END_VAR
```

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Project : PLCMaster

FUNCTION BLOCK : sModbusRTUMaster

Release : PLCMaster

Ver :1.00

Author :

Date:10/04/2012

Note :

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FUNCTION_BLOCK CPUModuleIO

(SFR054B000) Manages the logic I/O on the CPU module
 ENCRYPTED CODE

```

VAR_INPUT
Enable : BOOL; (* Function enable *)
D000 : BOOL; (* Digital output 0 *)
D001 : BOOL; (* Digital output 1 *)
END_VAR

VAR_OUTPUT
Enabled : BOOL; (* Function enabled *)
Done : BOOL; (* Function done *)
Fault : BOOL; (* Function fault *)
DI00 : BOOL; (* Digital input 0 *)
DI01 : BOOL; (* Digital input 0 *)
END_VAR
  
```

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	Project : PLCMaster	
	FUNCTION BLOCK : CPUModuleIO	
	Release : PLCMaster	Ver :1.00
	Author :	Date:10/04/2012
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(SFR054B000) Set the serial communication mode
ENCRYPTED CODE

```
VAR_INPUT
Fp : FILEP;
Baudrate : UDINT; (* Baudrate *)
Parity : STRING[ 1 ]; (* Parity type *)
DataBits : USINT; (* Nr of data bits *)
StopBits : USINT; (* Nr of stop bits *)
DTRManagement : USINT; (* DTR management type *)
DTRComplement : BOOL; (* Complement the DTR signal *)
EchoFlush : BOOL; (* Flush the echo *)
DTROnTime : UINT; (* DTR On wait time *)
DTROffTime : UINT; (* DTR Off wait time *)
END_VAR

VAR_OUTPUT
Done : BOOL; (* Execution done *)
Fault : BOOL; (* Execution fault *)
END_VAR
```

1

Project : PLCMaster

FUNCTION BLOCK : SetSMode

Release : PLCMaster

Ver :1.00

Author :

Date:10/04/2012

Note :

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