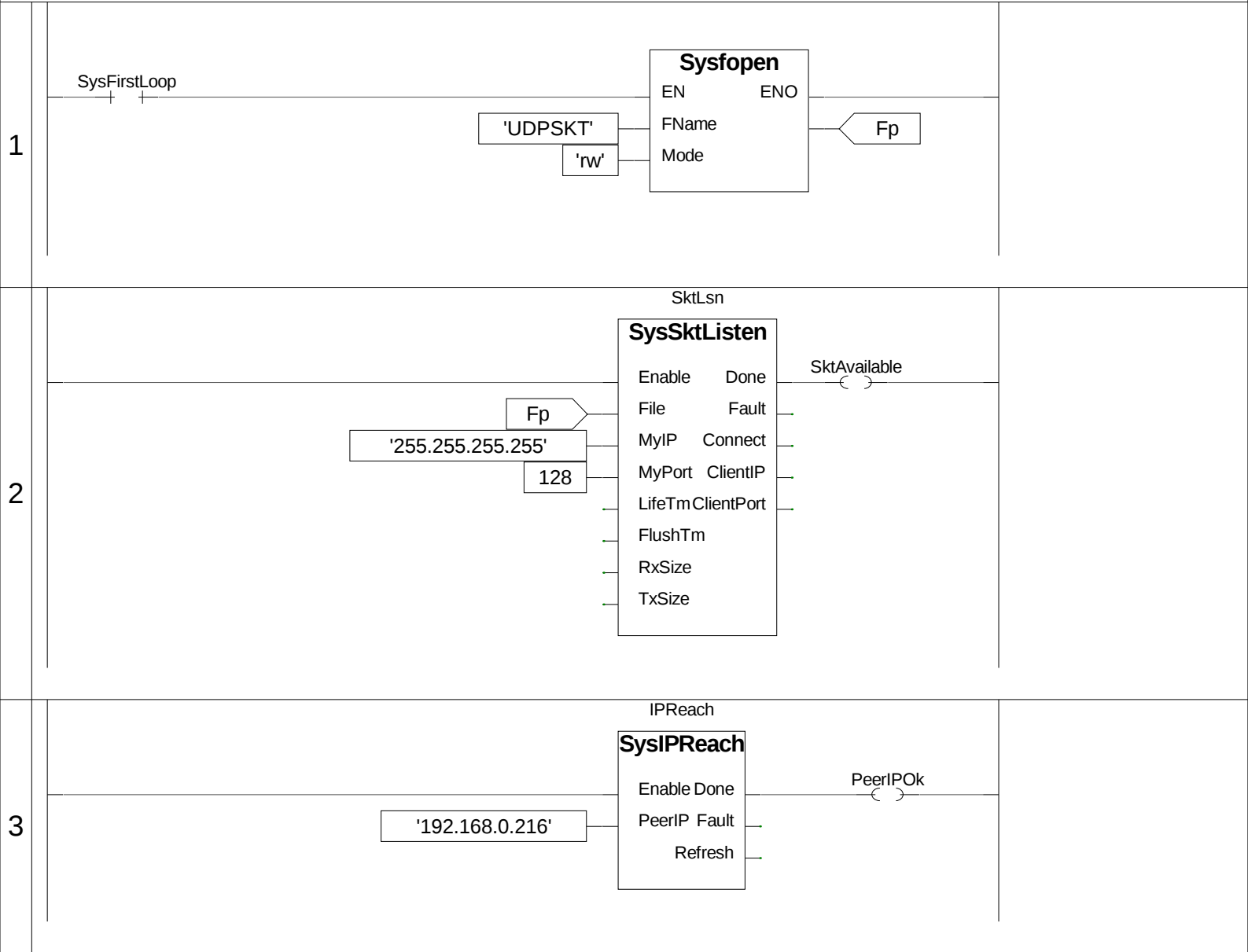
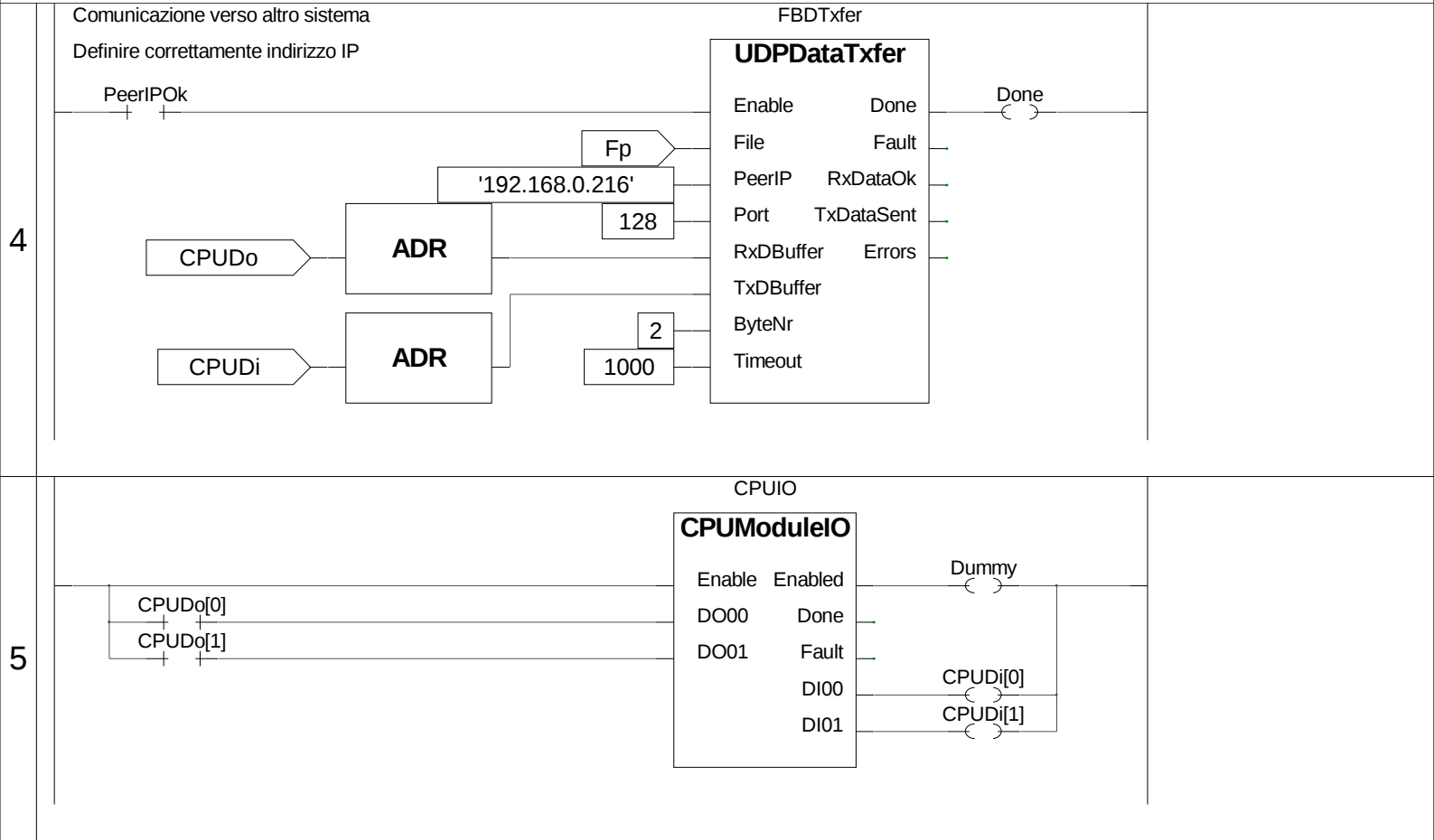


```
VAR
CPUDi : ARRAY[ 0..1 ] OF BOOL; (* CPU module digital input *)
CPUDo : ARRAY[ 0..1 ] OF BOOL; (* CPU module digital ouput *)
Done : BOOL; (* DataTxfer done *)
Dummy : BOOL; (* Dummy variable *)
SktAvailable : BOOL; (* Socket available *)
CPUIO : CPUModuleIO; (* CPU module I/O FB *)
Fp : FILE; (* UDP socket *)
SktLsn : SysSktListen; (* Socket listen *)
FBDTxfer : UDPDataTxfer; (* UDP data transfer *)
IPReach : SysIPReach; (* IP is reach *)
PeerIPOk : BOOL; (* Peer IP is reachable *)
END_VAR
```



	Project : CPUIOTxfer	
	PROGRAM : CPUIOTxfer	
	Release : CPUIOTxfer	Ver :1.00
	Author :	Date:11/02/2013
	Note :	Page:1 of 2



(SFR054B000) Manages the logic I/O on the CPU module
ENCRYPTED CODE

```
VAR_INPUT
Enable : BOOL; (* Function enable *)
D000 : BOOL; (* Digital output 0 *)
D001 : BOOL; (* Digital output 1 *)
END_VAR

VAR_OUTPUT
Enabled : BOOL; (* Function enabled *)
Done : BOOL; (* Function done *)
Fault : BOOL; (* Function fault *)
DI00 : BOOL; (* Digital input 0 *)
DI01 : BOOL; (* Digital input 0 *)
END_VAR
```

1

	Project : CPUIOTxfer	
	FUNCTION BLOCK : CPUModuleIO	
	Release : CPUIOTxfer	Ver :1.00
	Author :	Date:11/02/2013
	Note :	Page:1 of 1

(SFR054B200) Transfers data between systems by using UDP connection
ENCRYPTED CODE

```
VAR_INPUT
Enable : BOOL; (* FB enable *)
File : FILEP; (* UDP socket pointer *)
PeerIP : STRING[ 15 ]; (* IP adress *)
Port : UINT; (* Port number *)
RxDBuffer : @USINT; (* Rx data buffer *)
TxDBuffer : @USINT; (* Tx data buffer *)
ByteNr : UINT; (* Byte to exchange *)
Timeout : UINT; (* Timeout time (mS) *)
END_VAR

VAR_OUTPUT
Done : BOOL; (* Function done *)
Fault : BOOL; (* Function fault *)
RxDataOk : BOOL; (* Data received *)
TxDataSent : BOOL; (* Data sent *)
Errors : UINT; (* Communication errors *)
END_VAR
```

	Project : CPUIOTxfer	
	FUNCTION BLOCK : UDPDataTxfer	
	Release : CPUIOTxfer	Ver :1.00
	Author :	Date:11/02/2013
	Note :	Page:1 of 1