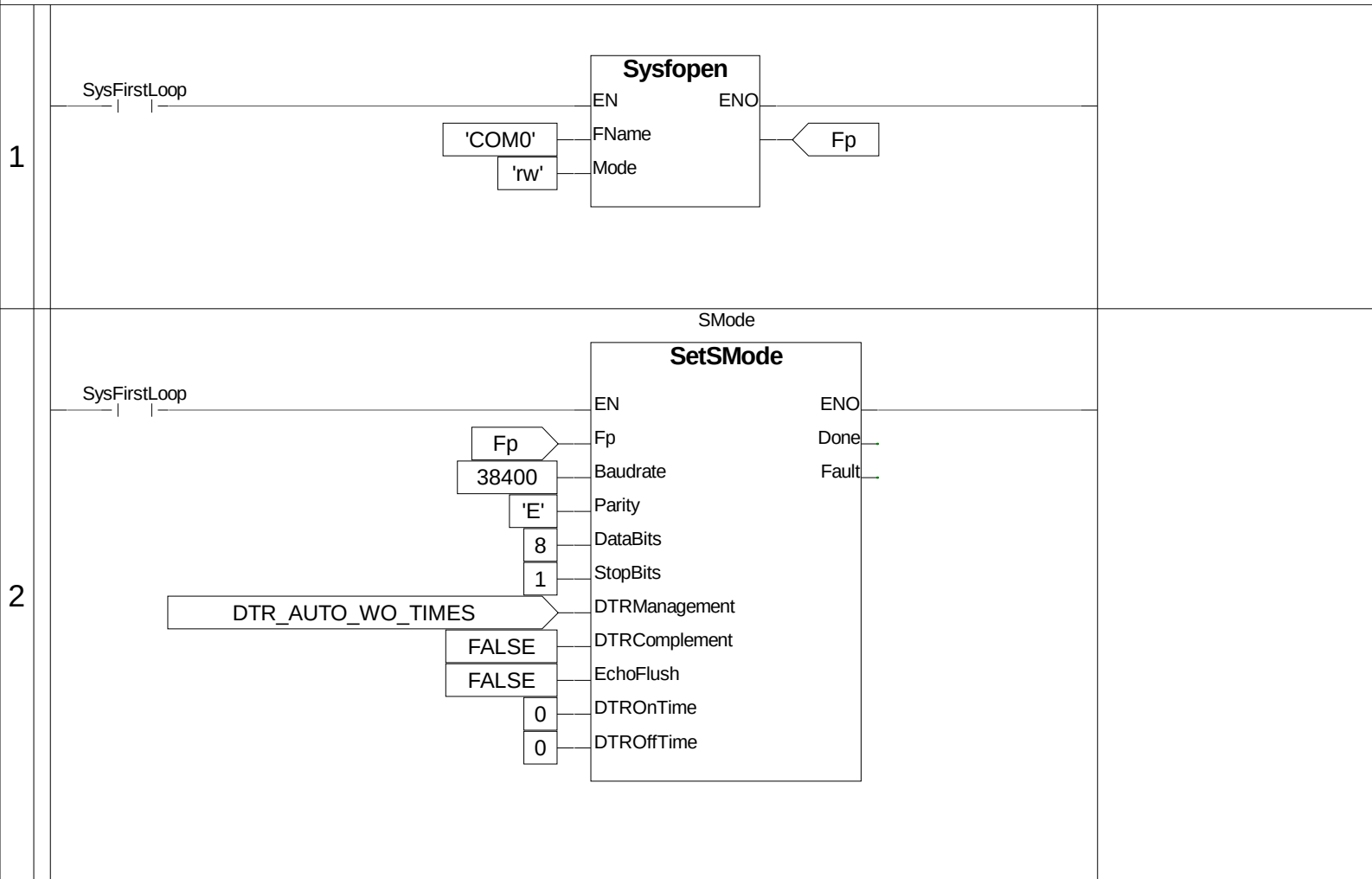


```
VAR
SMode : SetSMode; (* FB Set serial mode *)
Fp : FILEP; (* File pointer *)
Done : ARRAY[ 0..2 ] OF BOOL; (* Modbus Done *)
Reg1 : ModbusRTUMaster_v1; (* FB Modbus RTU *)
MachineID : UINT; (* Machine ID *)
VRMS_A_FL_MSW : UINT; (* Phase A Vrms (MSW) *)
VRMS_A_FL_LSW : UINT; (* Phase A Vrms (LSW) *)
Reg2 : ModbusRTUMaster_v1; (* FB Modbus RTU *)
Reg3 : ModbusRTUMaster_v1; (* FB Modbus RTU *)
END_VAR
```



Project : S203TA

PROGRAM : Communication

Release : S203TA

Ver :1.00

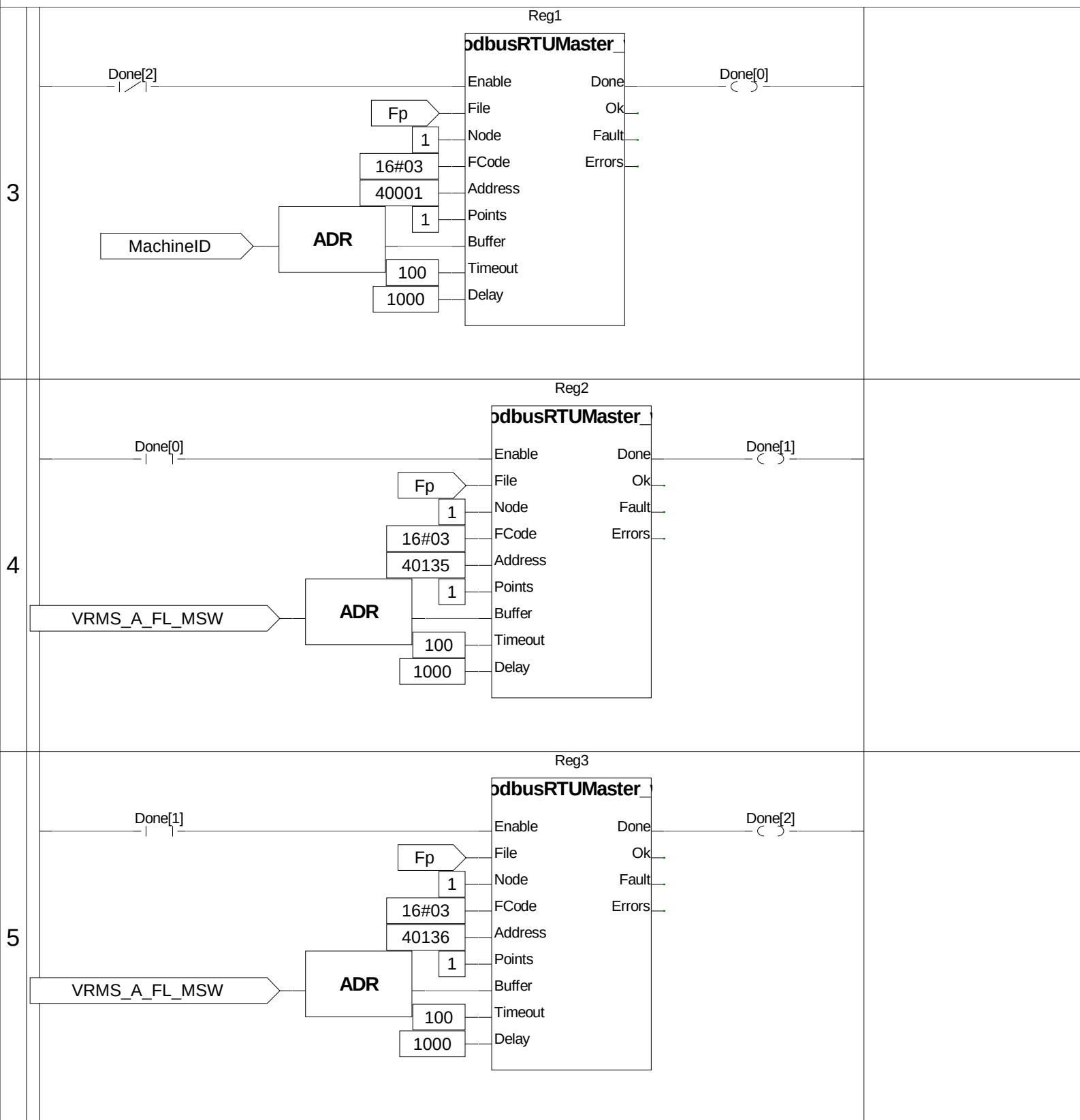
Author :

Date:11/02/2013

Note :

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# PROGRAM Communication



Project : S203TA

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Note :

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